

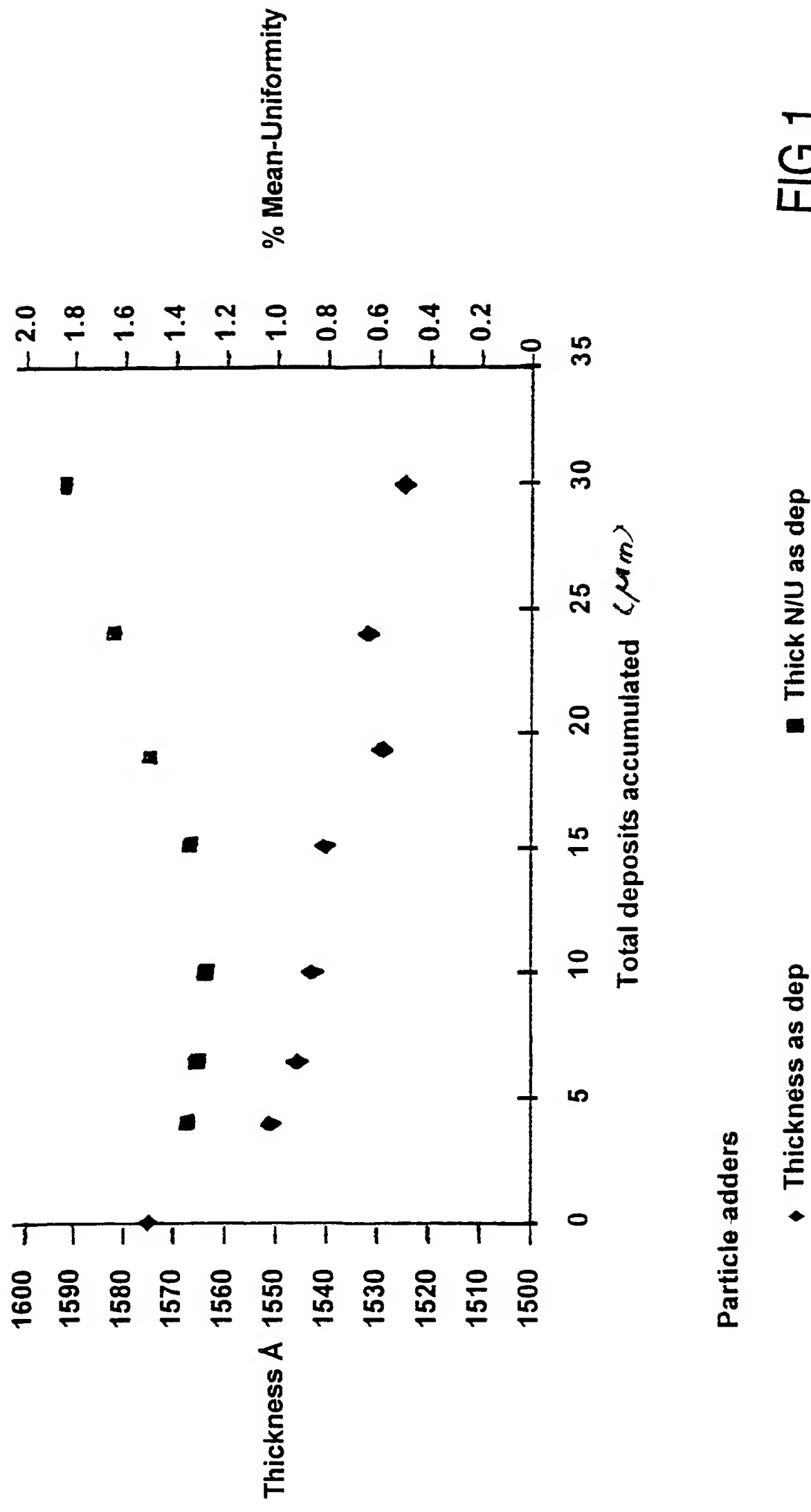
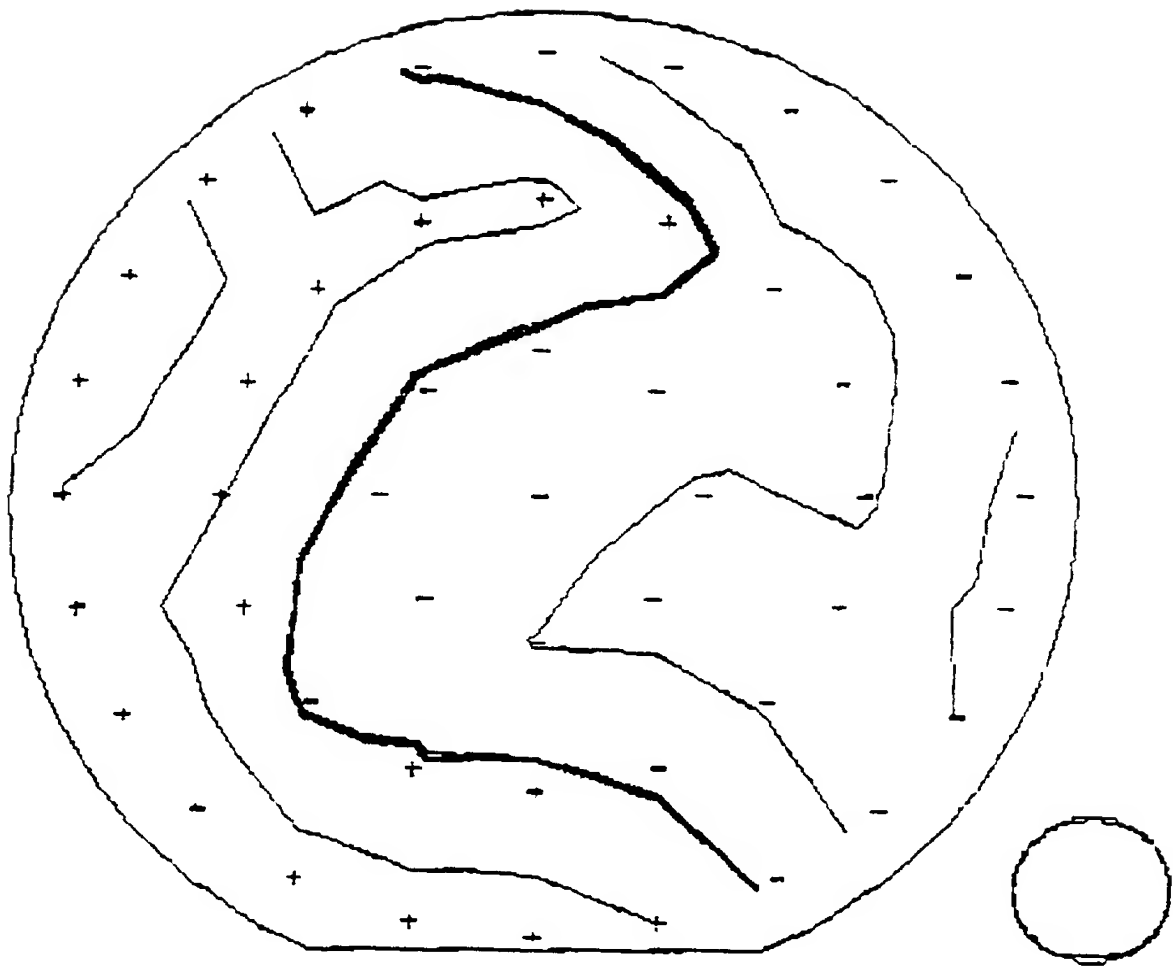
[illegible]

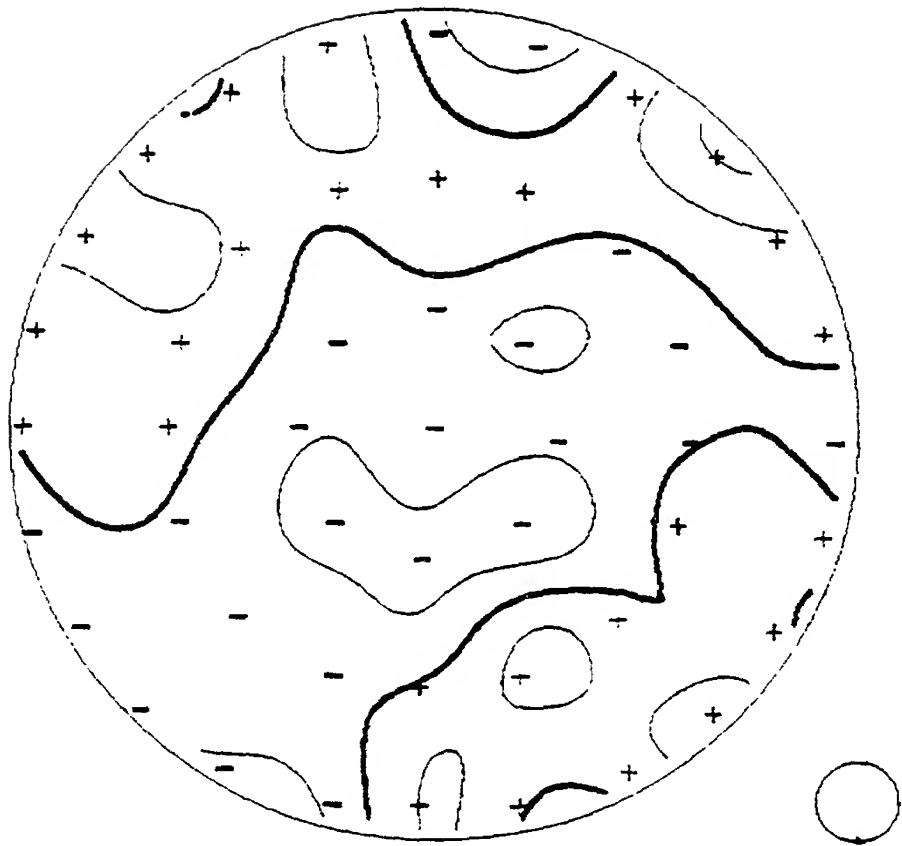
FIG. 1

2/15



MEAN	1138.6		WAFER DIAM	200.00
STD DEV	15.597	1.370%	TEST DIAM	194.00
MINIMUM	1111.1		TEMPLATE MAP	49 SITE
MAXIMUM	1172.4		CONTOUR DISP	STD
# SITES/GOOD	49/49		INTERVAL	1.000
SUBSTRATE	50MM (275 μ) Si		LAYER 2 FILM	POLY Si
SORTING SIGMA	3.0		MEAN-TARGET	0.000
CORRELATION	* NONE		WARNING	0.000
UNITS	ANGSTROMS		SPEC	0.000

FIG.2



- GOF Error
- ▲ Sigma Error
- Measurement Error
- Below Calculated Mean
- + Above Calculated Mean

Wafer Diameter	200 mm	Limits:	Percent
Edge Exclusion	3.000 mm	Mean Target	0.00 Å
Type/Pattern	Polar/49 Site Map	U/L Control	20.0%/20.0%
# Sites/Good	49/49	U/L Warning	5.0%/5.0%
Measurement	2 nd Thickness	U/L Sigma	3.00/3.00
Mean	1318.29 Å	Stdv Control	1.000
Std. Deviation	10.5104 Å / 0.797%	Contour Display	Percent
Minimum	1300.49 Å	Interval	11.0962
Maximum	1341.45 Å		
Range	40.95	Film Stack	Ox/aSi/Ox-T, T&n&k, T Copy
		Layer 3	Thermal SiO2
		Layer 2	# Amorphous Si
		Layer 1	Thermal SiO2
		Substrate	Silicon

FIG.3

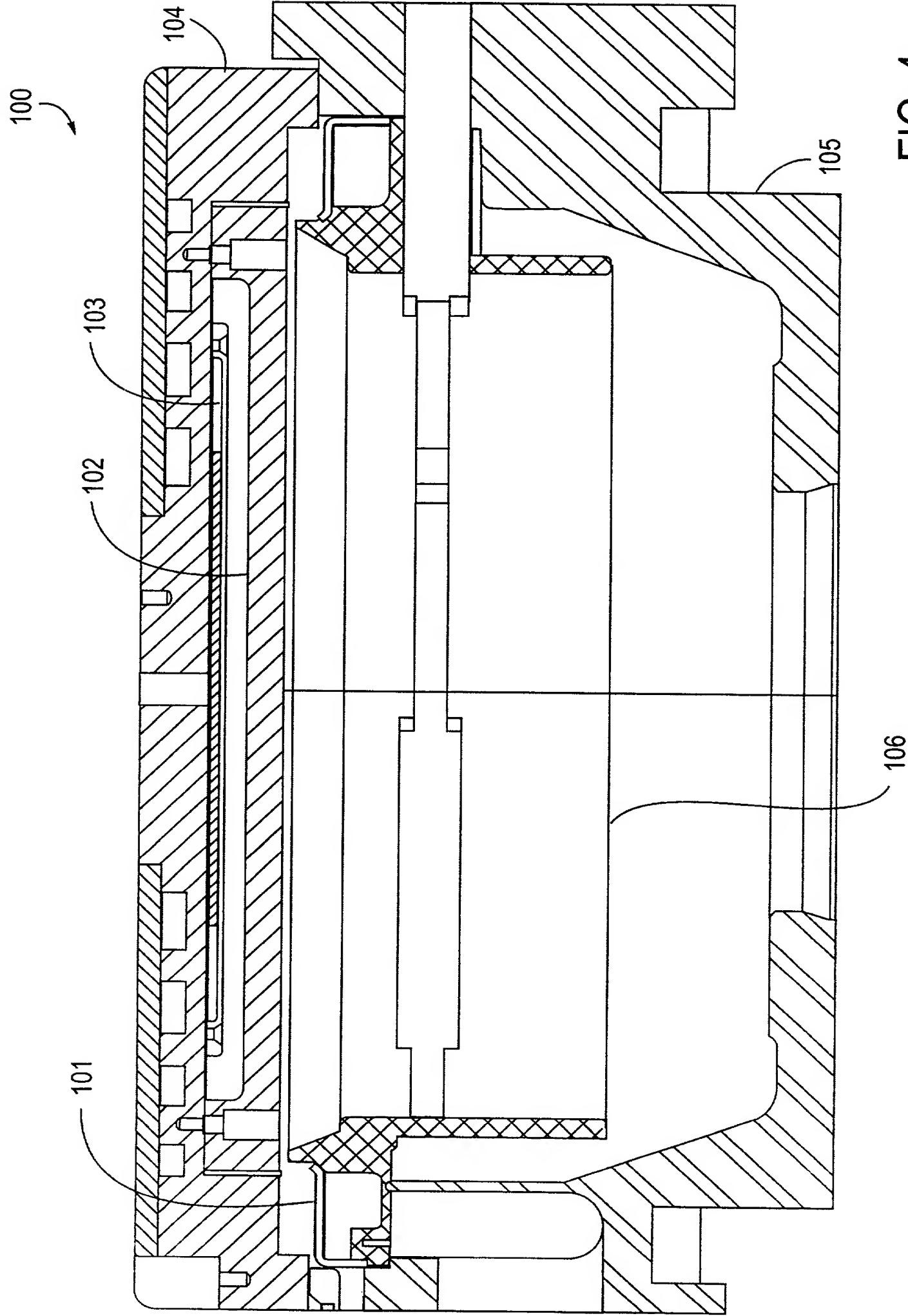


FIG. 4

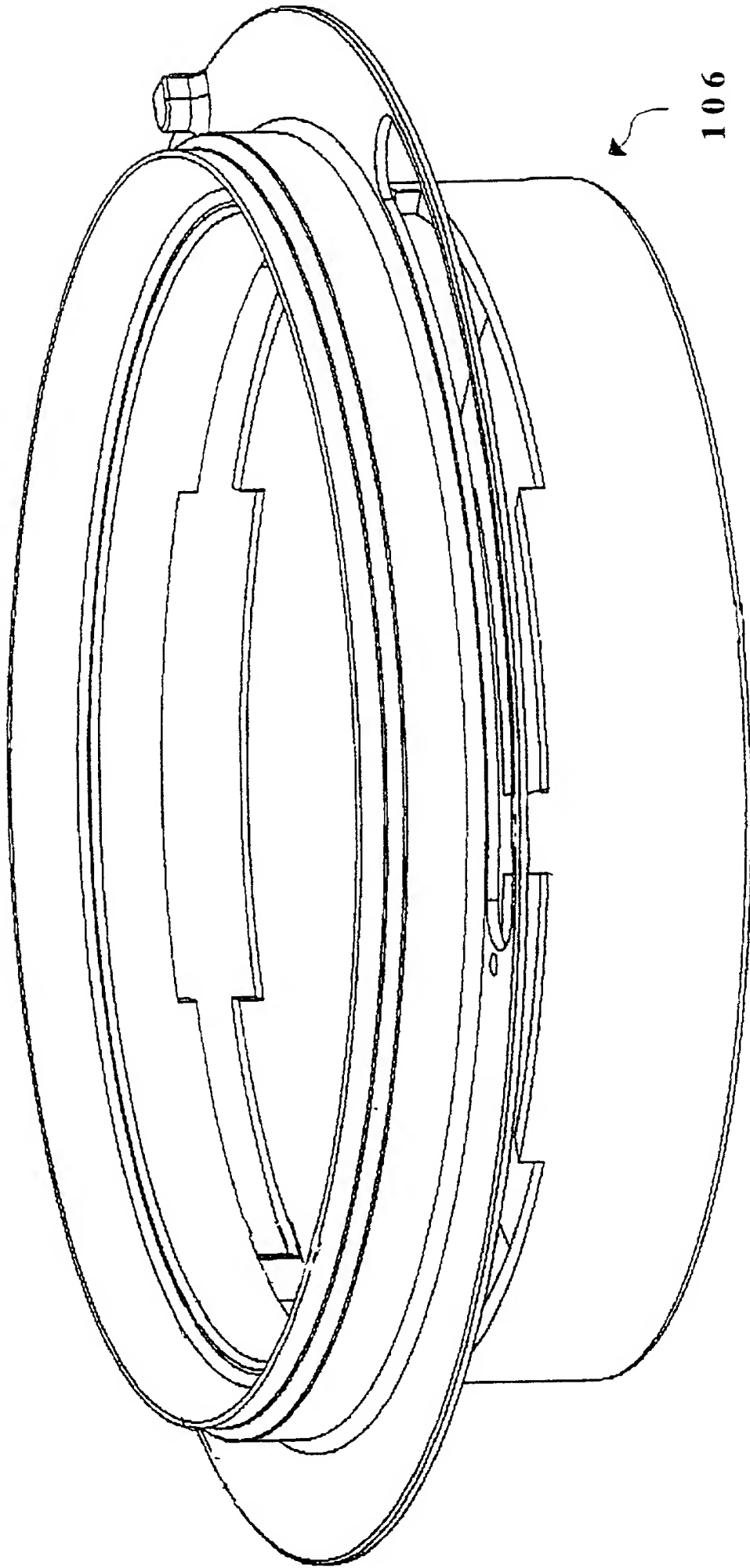


FIG.5

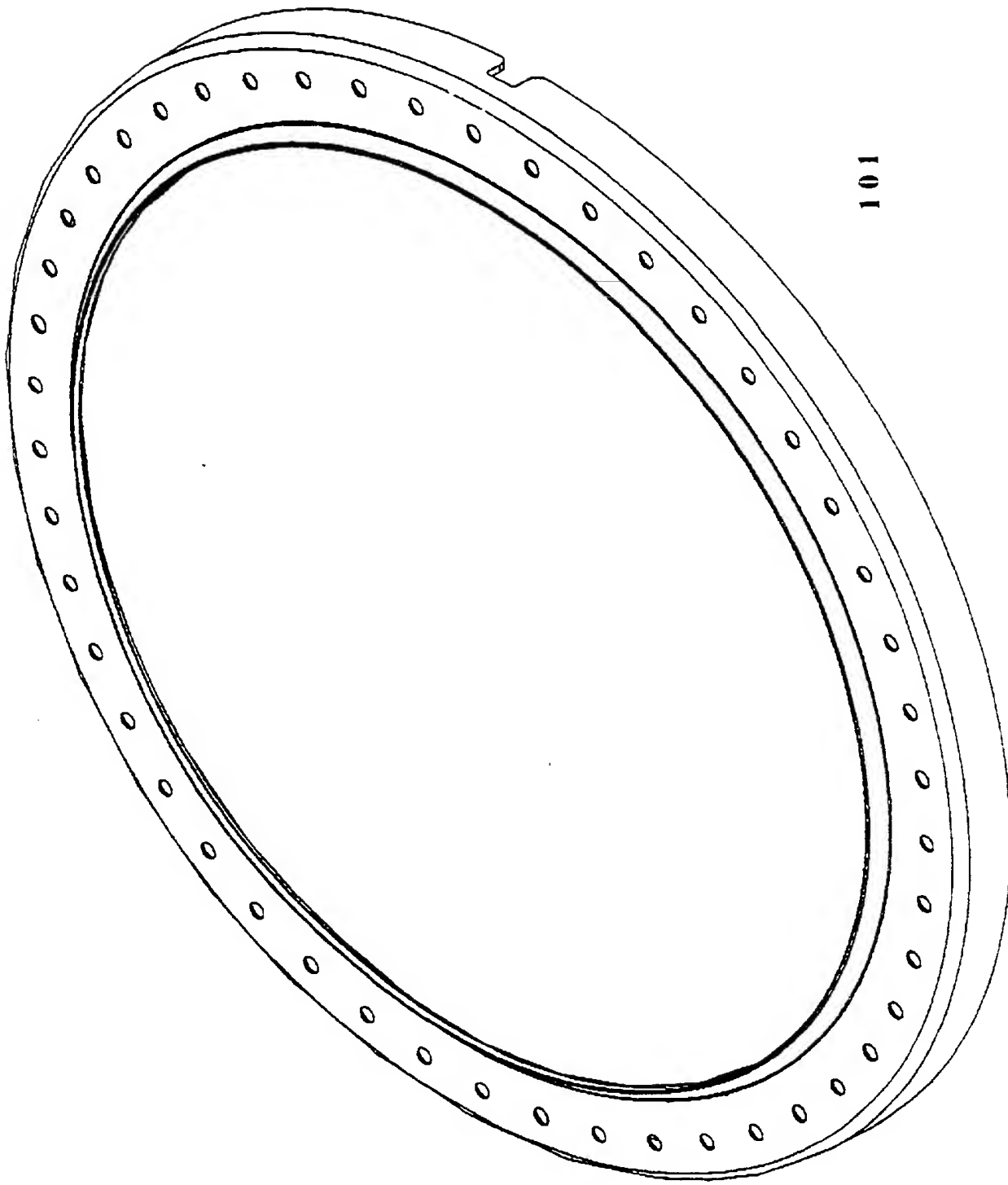


FIG.6

1345 1340 1335 1330 1325 1320 1315 1310 1305 1300

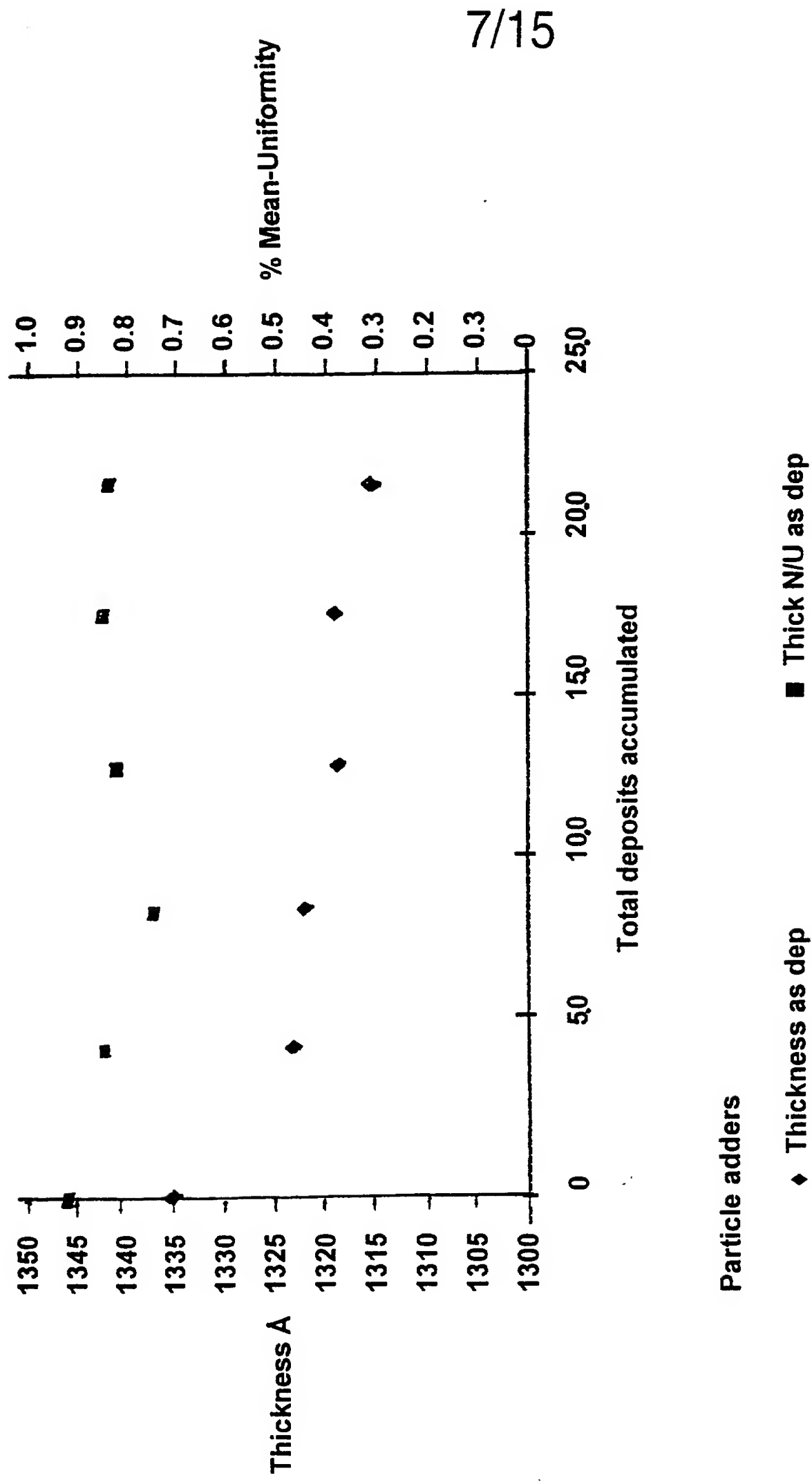


FIG.7

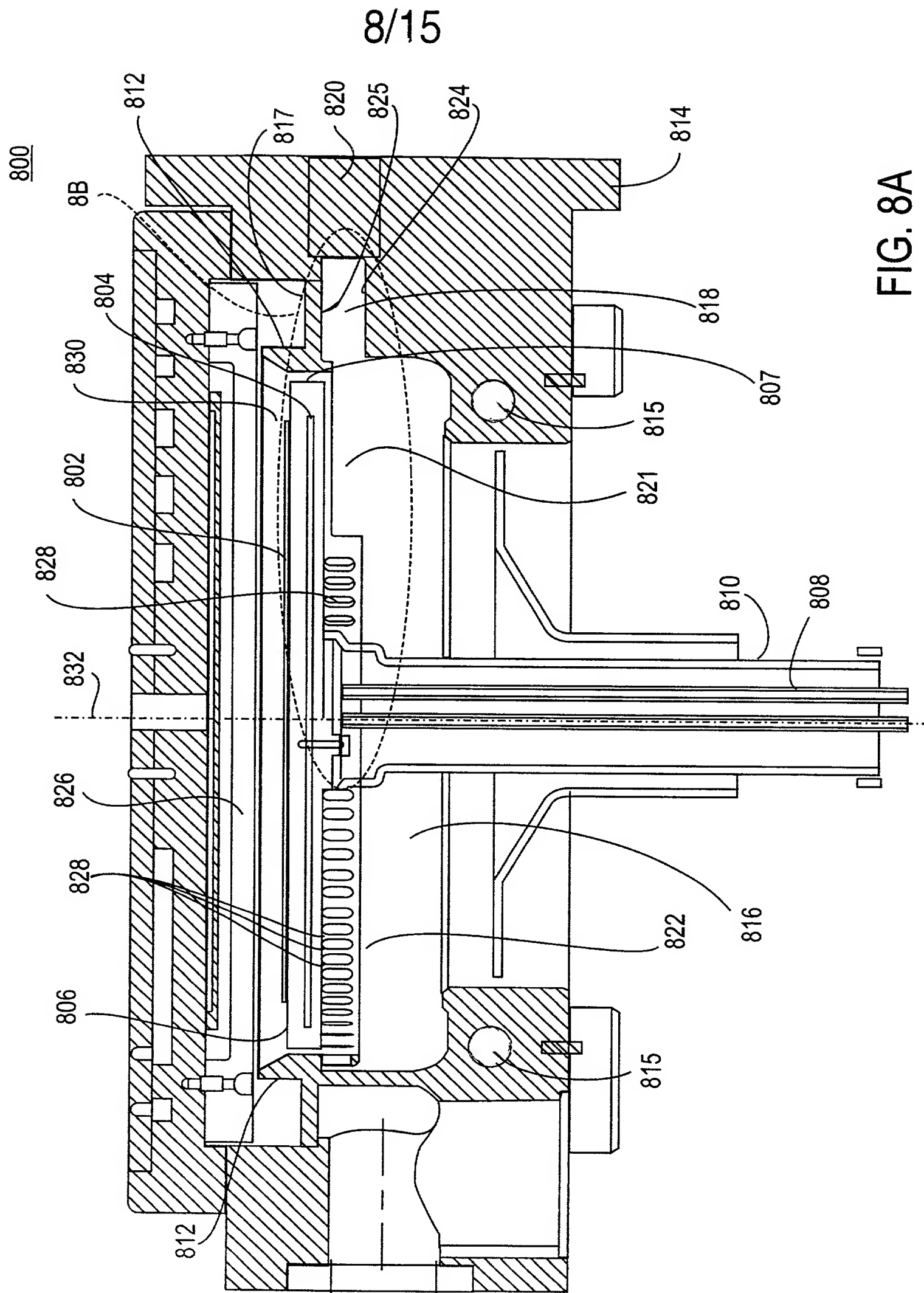


FIG. 8A



FIG. 8B

FIG. 8C is a cross-sectional view of the device 1000 in a second state, where the device 1000 is in a second state, and the device 1000 is in a second state.

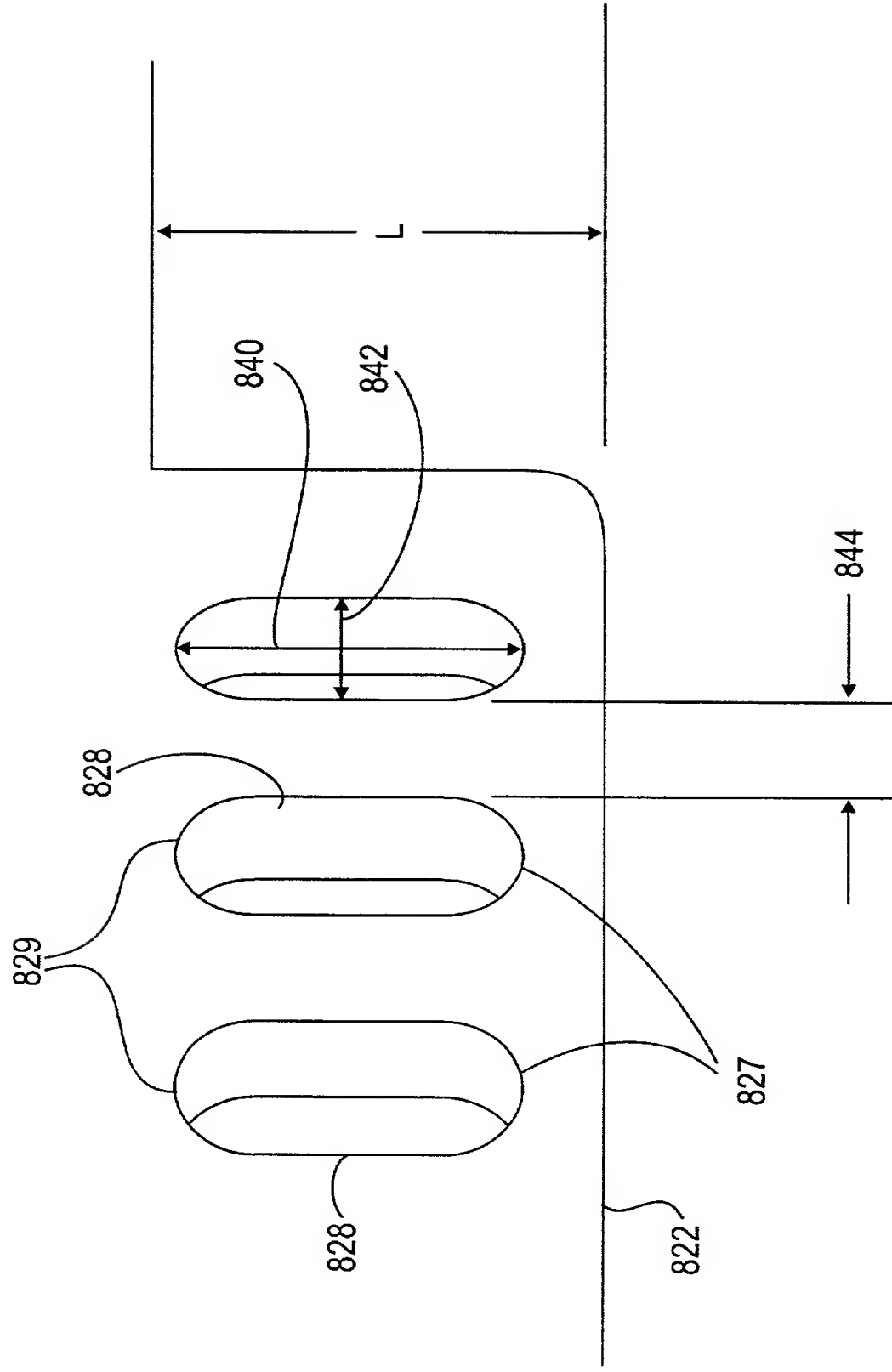


FIG. 8C

11/15

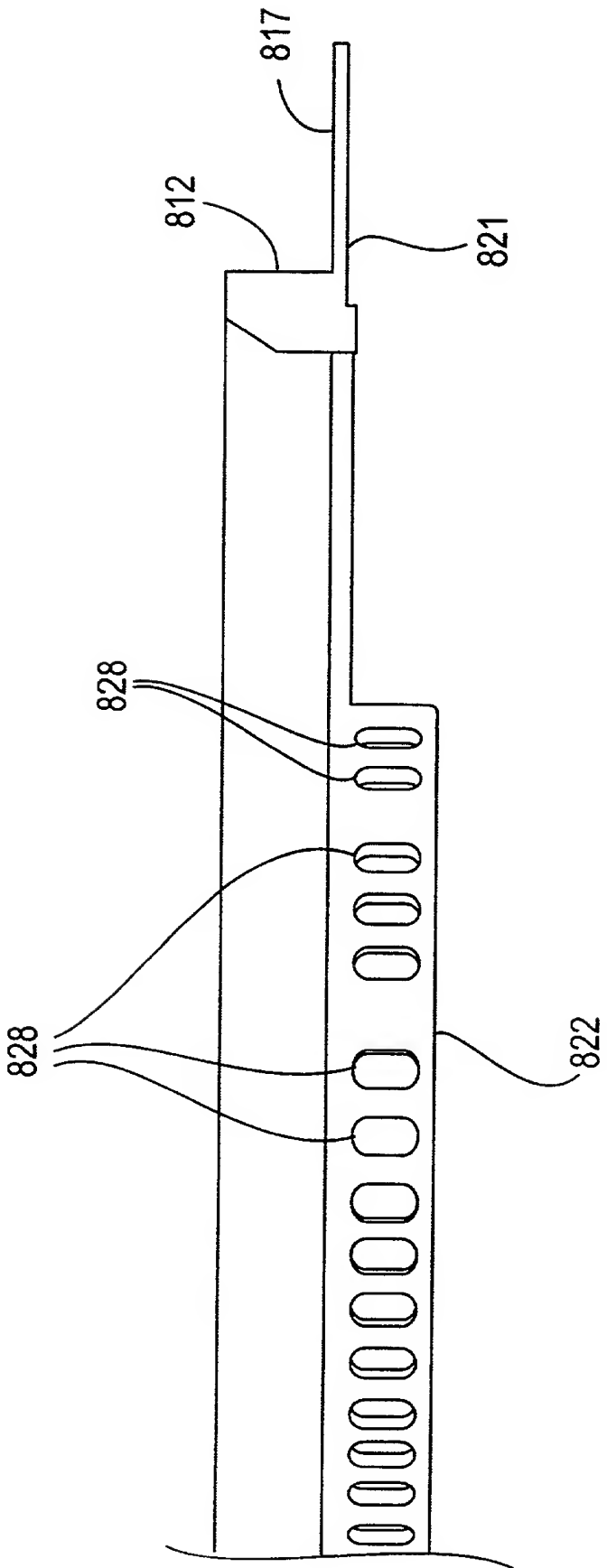


FIG. 8D

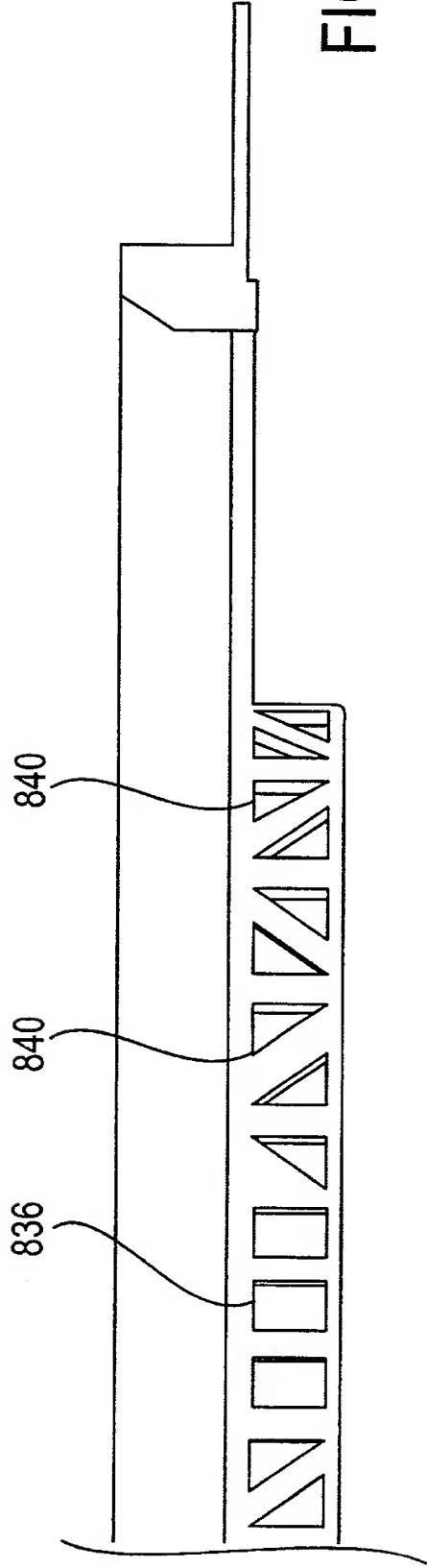


FIG. 8E

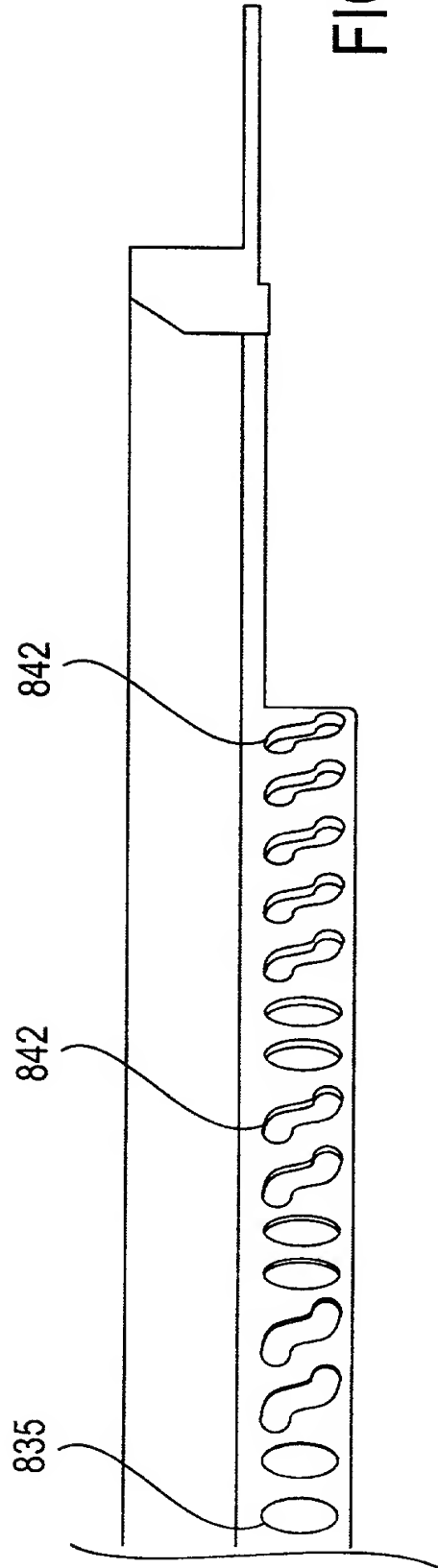


FIG. 8F

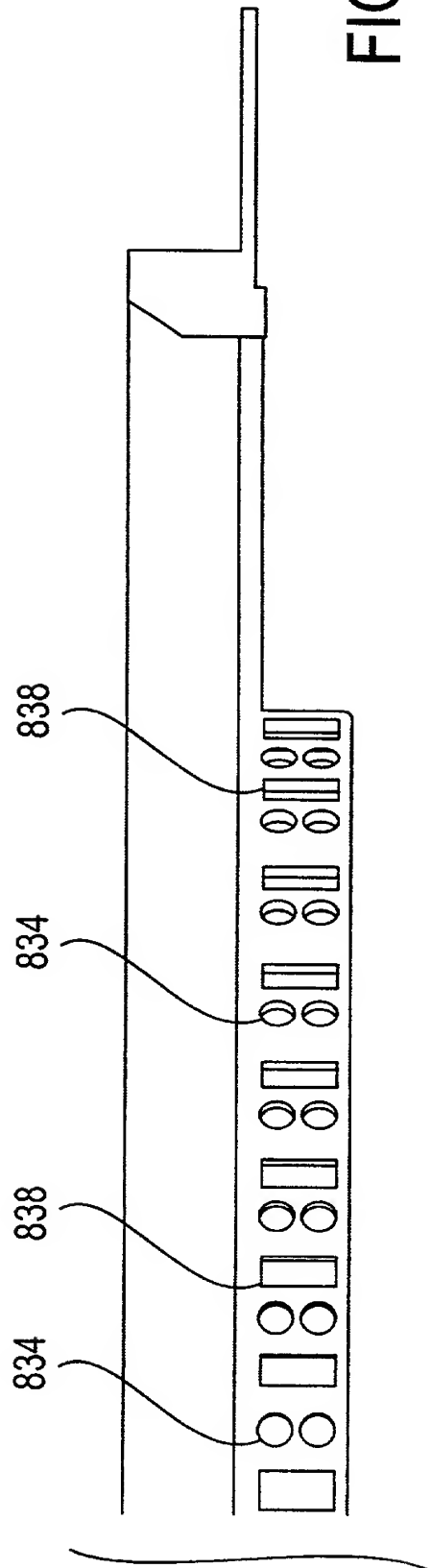


FIG. 8G

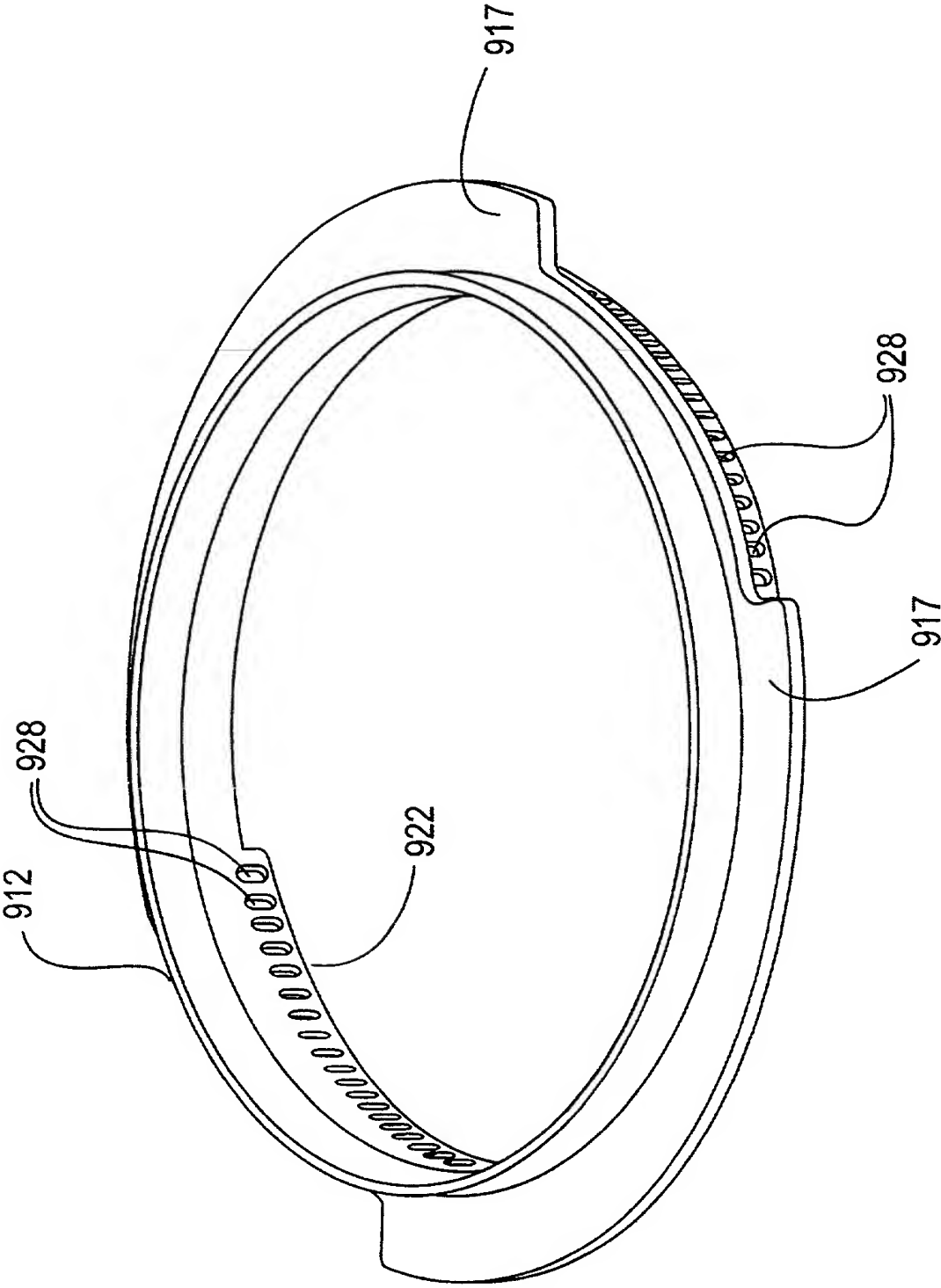


FIG. 9A

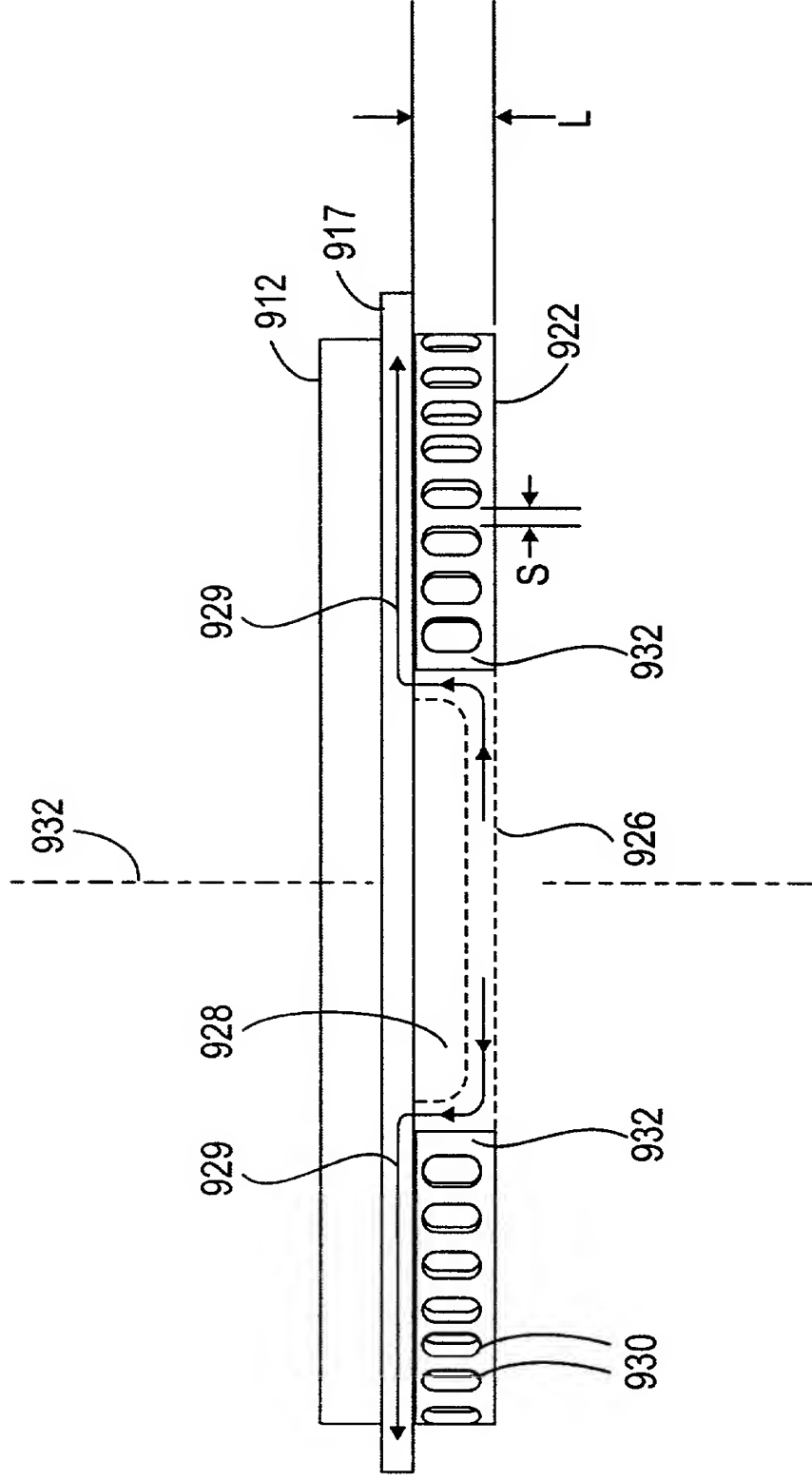


FIG. 9B

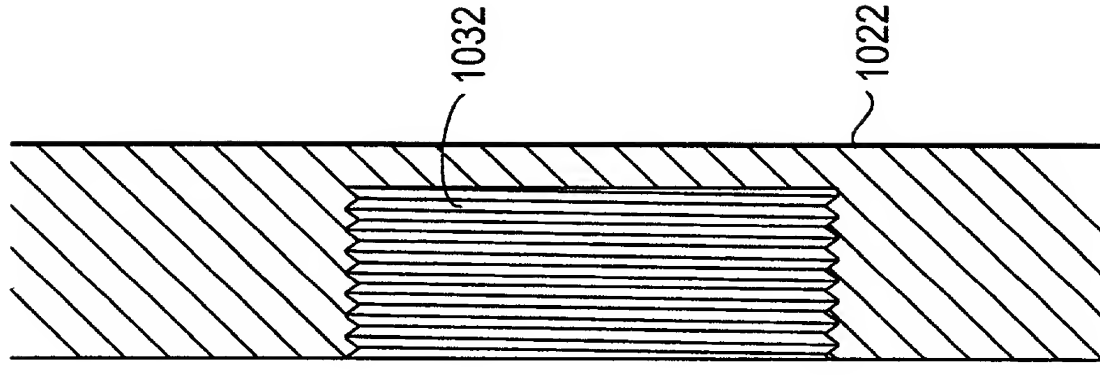


FIG. 10C

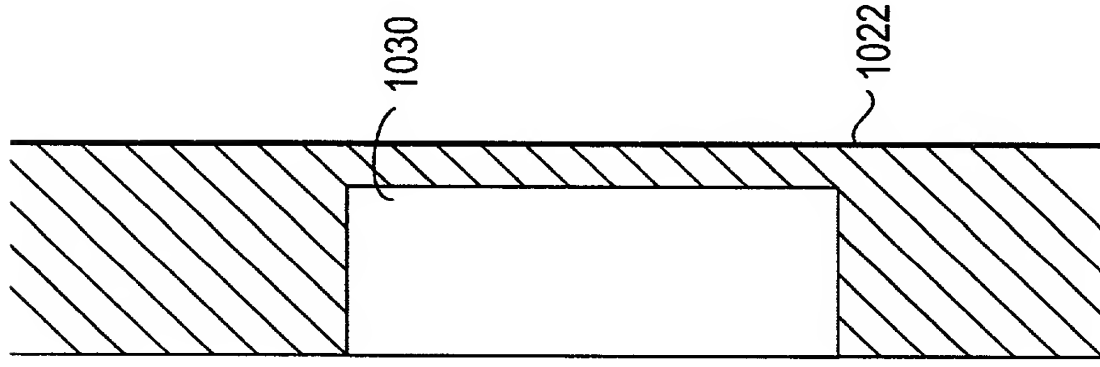


FIG. 10B

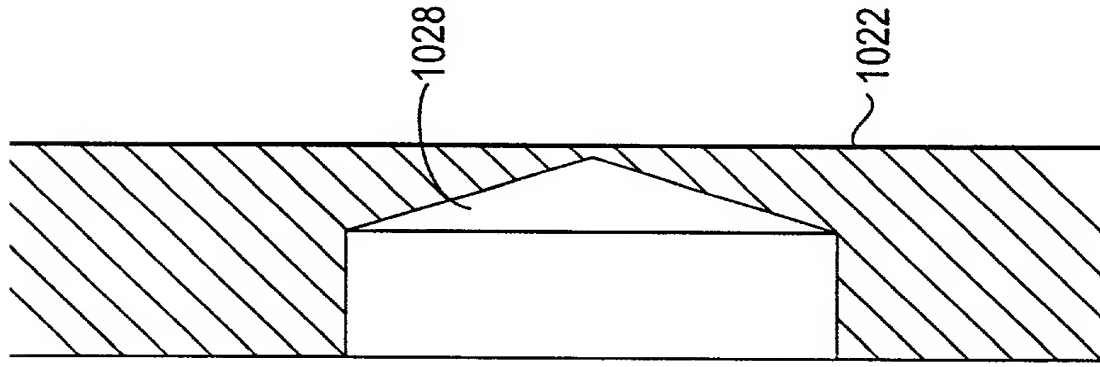


FIG. 10A